

ABOUT BARR GROUP

Mission: "Help as many people as possible build SAFER, MORE RELIABLE and MORE SECURE embedded systems."



The Embedded Systems Experts http://www.barrgroup.com



WEBINAR FORMAT

Overview and methodology Respondent demographics

Analysis

- Industry snapshot
- Safety findings
- Security findings

Prize winner announcement

A_BQ

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SURVEY GOALS

Barr Group's third annual market survey

- To deepen industry knowledge of trends and practices
- To help improve the embedded systems industry

Deep dive on safety/reliability and security

Thus a supplement to existing broad market surveys Similar demographics, but less "vendor" focused



SURVEY METHODOLOGY

Brief ~5-minute web-based survey



- Open Jan 10 to Feb 3, 2017
- Accessible via specific URL

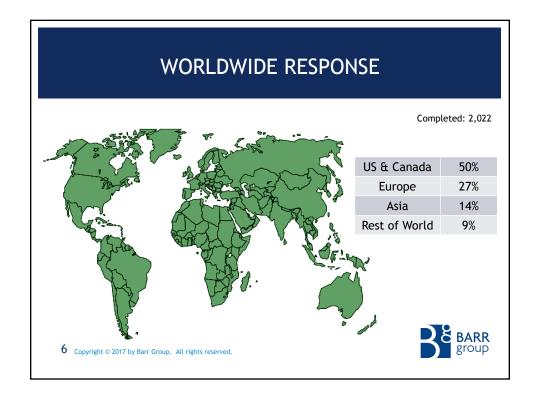
Nearly 200,000 targeted email invitations sent Link promotion on Twitter, LinkedIn, website, etc.

Prize drawing incentive

- Saleae USB logic analyzer (2 @ \$219 retail)
- Amazon.com gift card (3 @ \$25)







QUALIFICATION OF RESPONDENTS

Disqualifications based on

- No paid years of design experience (147)
- Not directly involved in designs (80)
- Vague current project details (69)

Qualified active professional engineers: 1,726!

- Study repeatability 95%
- Margin of error +/- 2.4%

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group

(SOME) PARTICIPATING ORGANIZATIONS

Agilent * Alcatel * Ametek * Apple * Aquatron Robotics

Battelle * Bayer Healthcare * Beta Bionics * Borg Warner * Bosch

Calsense * Carrier * Cisco * Continental Automotive * Cruzio

Daimler * Dolby * Eaton * Echostar * Fluke * Ford * Fresenius Medical

Garmin * Goodyear * Graco * Grundfos * Harman * Harris * Honeywell

Hughes * IBM * Intel * JHU-APL * John Deere * Keysight * L-3 Communications

Landis+Gyr * Lenovo * Lincoln Electric * Lockheed Martin * Lutron

MED-EL * Medtronic * Mitre * Motorola * NCR * NDI Medical * Netapp

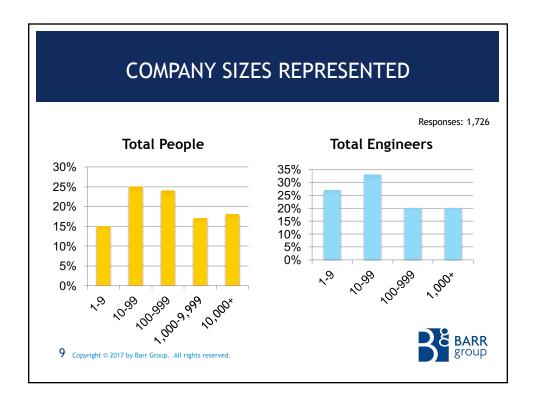
Northrup Grumman * Omron * Orthoscan * Overhead Door * Philips * Phytec

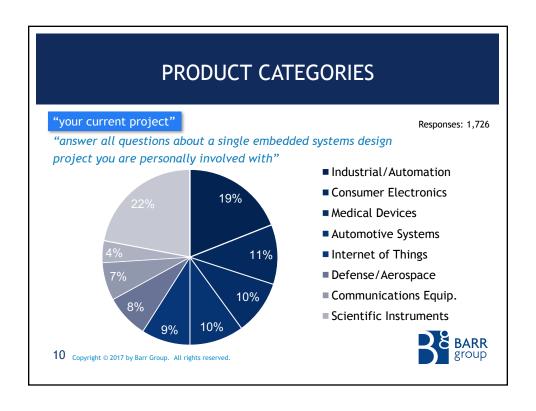
Qualcomm * Renesas * Rockwell * Rockwell Collins * Schneider Electric

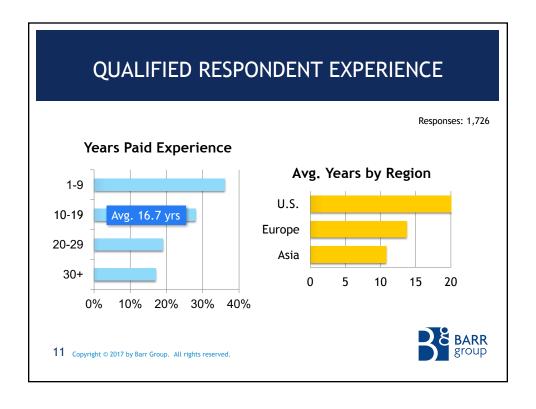
Schonstedt * Sciex * Seagate * Shlumberger * SnapOn * Spirent * Stryker

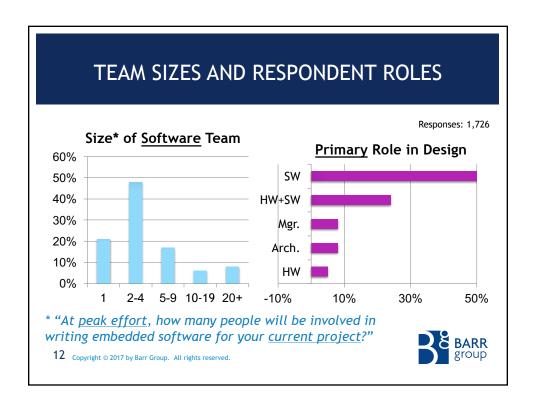
Teledyne * Thales * Thermo Fisher * Texas Instruments * Toshiba

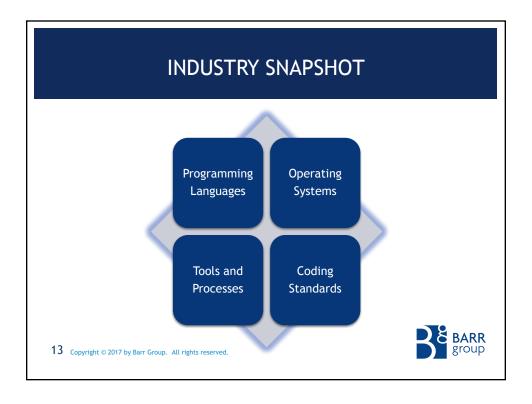
Tyco * Visteon * Wavetronix * Whirlpool * Xerox * Zebra

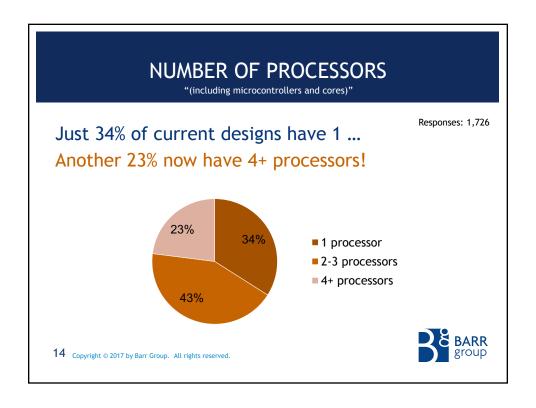


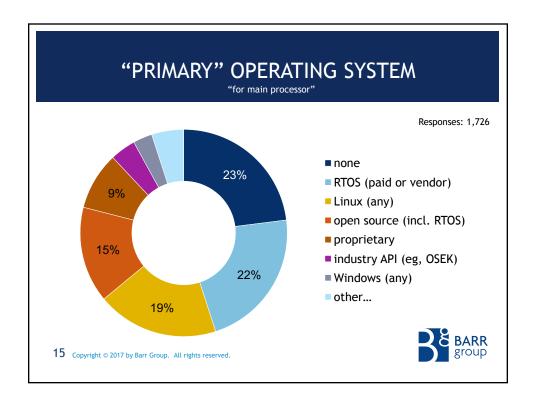


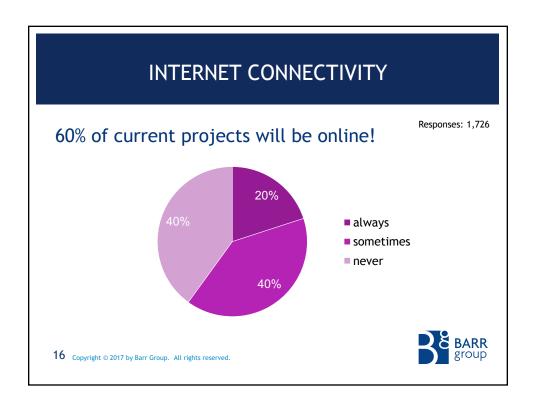


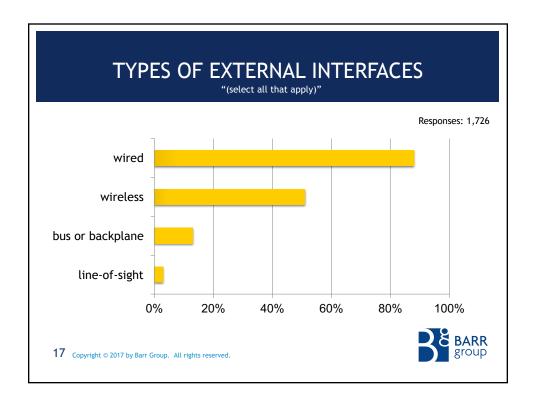


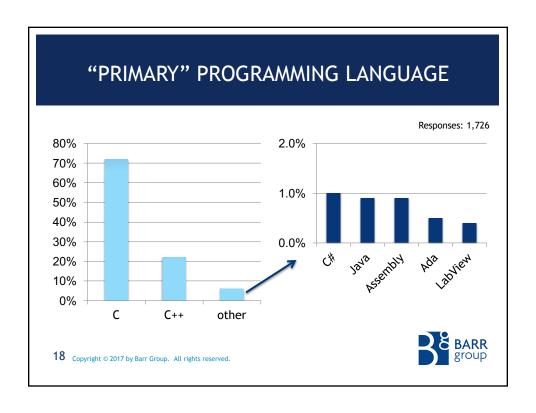


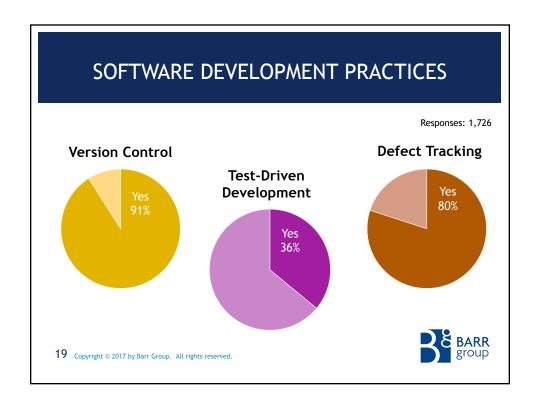


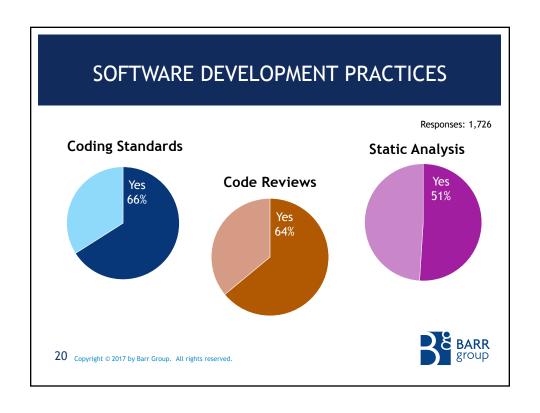


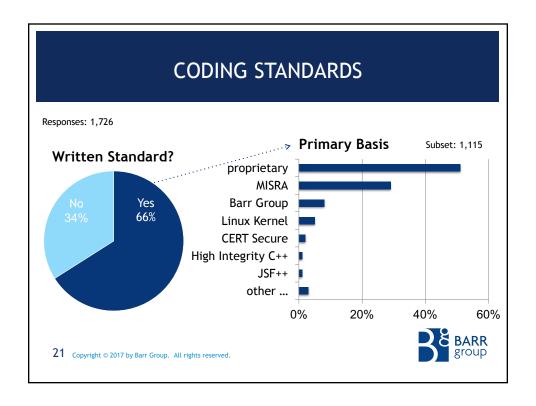


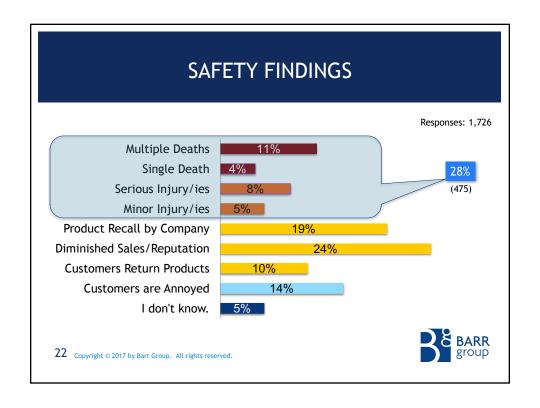


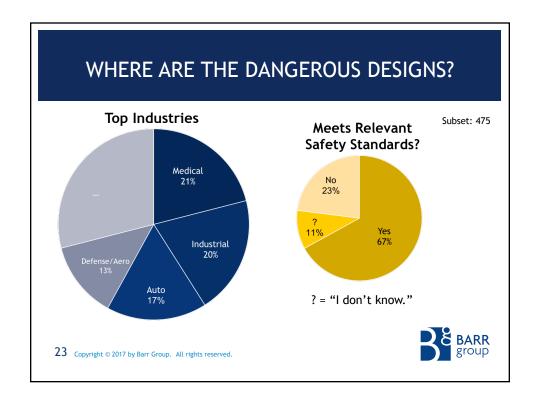


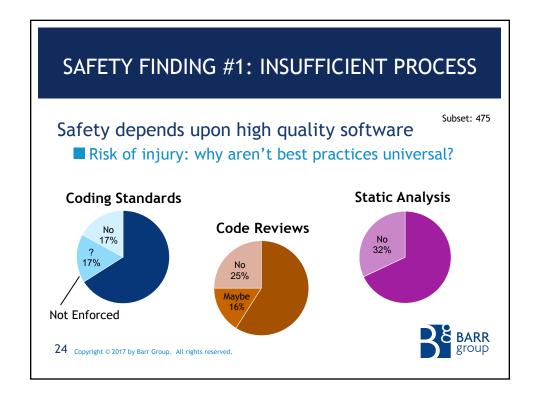


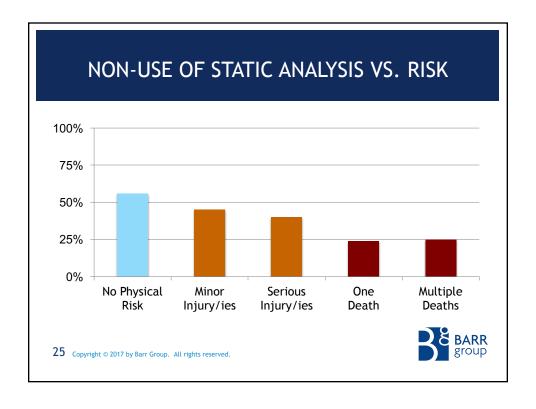


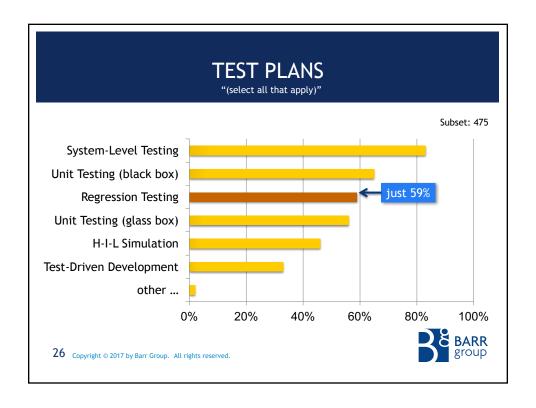












RISK SHOULD DICTATE PROCESS

Safety, like justice, must be seen to be present...

■ Written "safety case" analysis

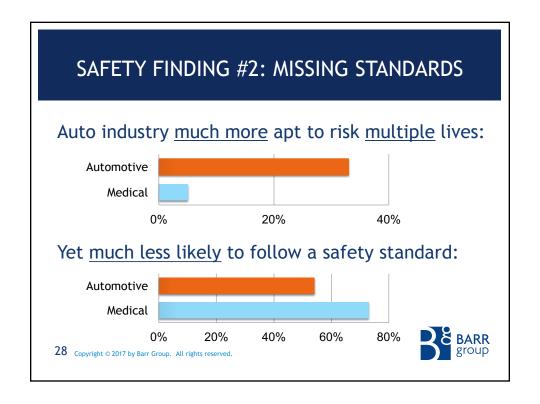
The graver the risk, the greater the needs

■ Worst-case risk → "Safety Integrity Level" → process (e.g., MISRA-SW requires code "review" at SIL2+ and "automated static analysis" at SIL3+)

System/software reliability must be "baked in"

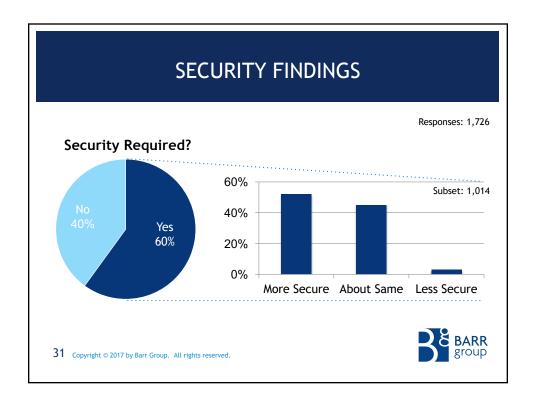
■ Safety can't be a "bolt on" feature

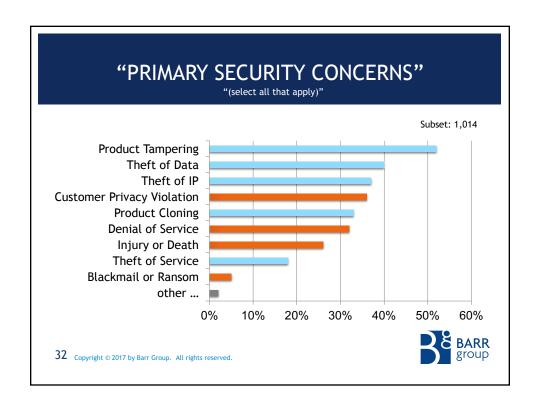


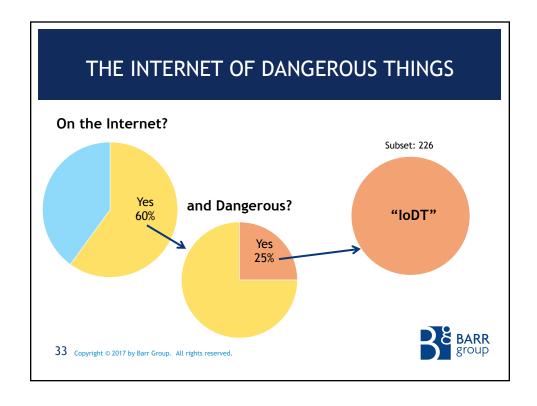


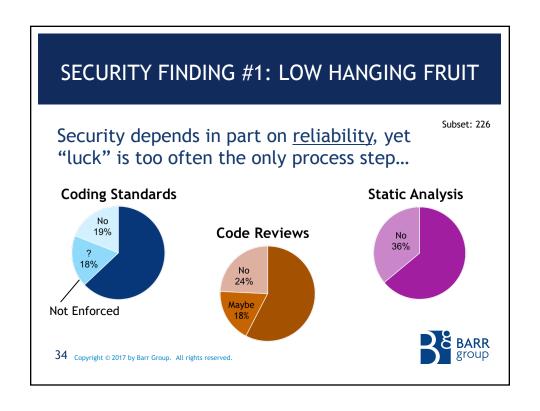


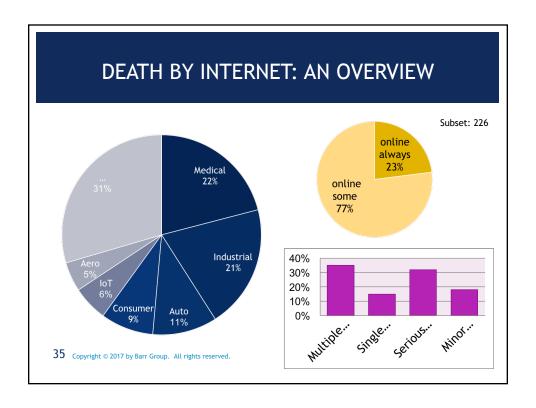


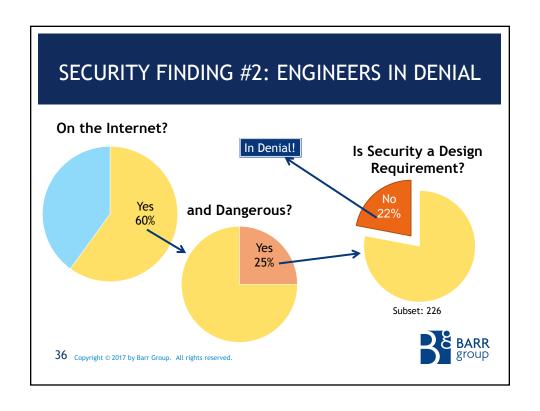


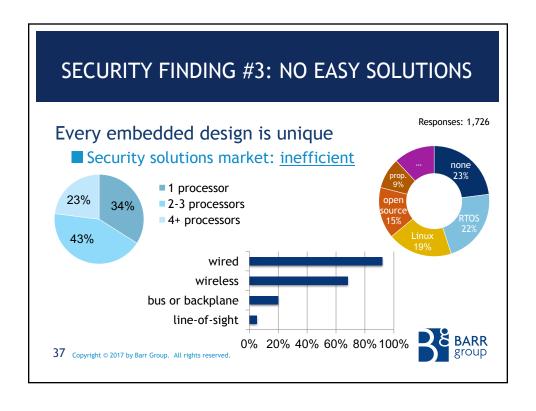














QUESTION & ANSWER

We'll now answer questions from attendees...



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THANK YOU FOR JOINING US!

Note: This webinar is a summary of key findings

- Written report available as free PDF (see website)
- Raw survey data available for license (contact us)

Upcoming public training events

- Best Practices for Designing Safe & Secure Systems
- Embedded Security/Android/Software Boot Camps
- Several courses in Munich, Germany

http://barrgroup.com/training-calendar

